

257	3471	Subclass
ISSUE CLASSIFICATION		

PATENT NI

U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>SCANNED <i>15-11</i> Q.A. <i>Am</i></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/941761	CONT/PRIOR	CLASS 438	SUBCLASS	ART UNIT 2811	EXAMINER
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APPLICANTS

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TITLE

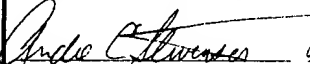
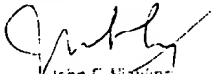
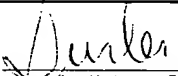
Method and structure for temporarily isolating a die from a copper conductor to facilitate wafer level testing

PTO-2040
12/99[illegible]

901103

Number of drawings: 3 sets set 1

12/17/01

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg. 78	Figs. Drwg. 12/2	Print Fig. #2	Total Claims 47
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	 (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED 05/20/03	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____		ISSUE FEE 1h Amount Due \$1600	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	 (Primary Examiner)		Date Paid 8-20-03	
	 (Legal Instruments Examiner)		ISSUE BATCH NUMBER	

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